



TFW  
2633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:

Kannan Raj et al.

Serial No.: 09/839,023

Filed: April 20, 2001

For: Optically Interconnecting  
Multiple Processors

§  
§  
§  
§  
§  
§  
§  
§  
§

Art Unit: 2633

Examiner: Dalzid E. Singh

Docket: ITL.0462US  
P9816

Assignee: Intel Corporation

Mail Stop **Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 20050330**

Sir:

In response to the office action mailed November 14, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: January 12, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
Cynthia L. Hayden